

AUG 16 2006

PATENT
Atty. Dkt. No. NVDA/P000785**REMARKS**

This amendment is submitted in response to the Office Action dated May 19, 2006. Reconsideration and allowance of the claims is requested.

In this Office Action, claims 1-3, 10-13 and 17 are rejected under 35 U.S.C. 102(b) as anticipated by Boucher (U.S. 6,334,153). The remaining claims 4-9, 14-16, and 18-20 are rejected under 35 U.S.C. 103 as unpatentable over Boucher in view of Applicant's Admitted Prior Art (AAPA). These rejections are respectfully traversed.

The claims have been revised to clarify the dual-track nature of the claimed approach to uploading frame data which is transmitted for TCP processing. As claimed, a hardware subsystem is configured to process frames of certain delegated connections selected by a TCP stack to produce frame data and store in system memory. Under certain circumstances, such as if a user buffer is not available, then the hardware subsystem will store the frame data in a legacy buffer for separate later processing by a TCP stack executing on a central processing unit.

The claims now also further recite a system memory comprising a connection table for storing data tracking the active connections within the system, including the delegated connections. Nothing similar to the TCP stack, the legacy memory and the system memory with connection table can be found in the references relied on. Further, the steps now clearly recited in the claims of the present application cannot be found in either Boucher, or in the cited paragraph [0006] of Applicant's own application which teaches and acknowledges only direct frame processing and storage.

In reviewing the Examiner's rejection, it is also apparent from his paragraphs 7, 8 and 9, that he concedes that cited reference, Boucher, does not teach a system where the hardware subsystem is configured to process frames to produce partially processed frame data, and then upload the partially processed data to the legacy buffer for later processing.

The Examiner relies solely on the Applicant's Admitted Prior Art, especially paragraph [0006], to supply this deficiency. However, neither Boucher nor the AAPA suggests that the hardware may partially process a frame of data, and then depending

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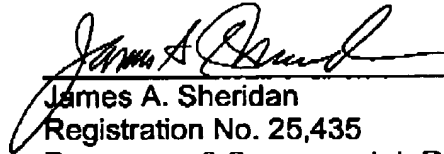
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on the result of that processing (see paragraphs [0053]-[0056]), transmit the partially processed frame to the TCP stack for completion of the processing.

In view of these clear and significant distinctions, reconsideration and allowance of the claims is requested.

Respectfully submitted,



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